

Docket No. 501.39158CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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Applicants: N. HASEGAWA, et al.

SEP 28 2005

Application No.: 10/770,413

Filed: February 4, 2004

For: MANUFACTURING METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICES

Art Unit: 1756

Examiner: Deborah Chacko Davis

AMENDMENT**Mail Stop: Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 28, 2005

Sir:

In response to the Office Action mailed March 28, 2005, please amend the above-identified application as listed in the following and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.